

REMARKS

Claims 1-68 are pending in the application. Claims 1-68 are rejected. Claims 1, 10, 11, and 66-68 are amended herein. No new material is added by the amendments herein. Applicants respectfully submit that claims 1-68, as amended herein, are 5 patentably distinct from the cited prior art and the prior art made of record, and in view of the amendments and remarks herein the rejections have been overcome. Thus, Applicants respectfully request withdrawal of the rejections.

Claim Rejections Under 35 USC §102

10 Claims 1-10, 15-18, 21, 25-32, 34-47, and 52-68 are rejected under 35 USC §102(e) as being anticipated by Bowman-Amuah, United States Patent number 6,697,824 ("Bowman-Amuah"). Applicants submit that Bowman-Amuah discloses a system and method for interacting with a user over a network for personalizing a website (column 2, lines 16-17). The system disclosed in Bowman-Amuah is described as being practiced in 15 the context of a personal computer such as an IBM-compatible personal computer, Apple Macintosh computer or UNIX-based workstation (column 4, lines 48-52; figure 1).

Applicants respectfully submit that Bowman-Amuah does not disclose or teach a method for host vehicle internetworking, comprising coupling a plurality of network elements in a vehicle including at least one node and at least one vehicle bus among at 20 least one peripheral electronic device, wherein the at least one node includes at least one gateway node in the vehicle, the gateway node comprising a first processor performing real-time processes and a second processor performing remaining processes other than the real-time processes, manipulating node information including configuration and security information, automatically assembling and configuring the plurality of network 25 elements in response to the node information, remotely controlling at least one function of the plurality of network elements, and providing secure interoperability among the plurality of network elements in response to the node information (underlining represents emphasis added). Support for Applicants' position follows.

Referring to claim 1, as amended herein, Applicants respectfully submit that 30 Bowman-Amuah does not teach or disclose a method comprising coupling a plurality of network elements in a vehicle including at least one node and at least one vehicle bus

among at least one peripheral electronic device. In contrast to amended claim 1, Bowman-Amuah describes a "Delivery Vehicle" as an integrated collection of technology services that supports an application style, implemented on a distinct architecture generation (column 17, lines 27-30). However, Applicants do not find any
5 mention in Bowman-Amuah of a vehicle that is not a collection of technology services (Delivery Vehicle), and further a vehicle that includes the network elements like the node and the vehicle bus of claim 1. Therefore, Bowman-Amuah does not disclose a plurality of network elements in a host vehicle including at least one node and at least one vehicle bus among at least one peripheral electronic device

10 Applicants submit that, in further contrast to amended claim 1, Bowman-Amuah does not teach or disclose that the node includes a gateway node in the vehicle, the gateway node having multiple processors comprising a first processor performing real-time processes and a second processor performing remaining processes other than the real-time processes. While Bowman-Amuah discloses software running in the context of
15 a personal computer such as an IBM-compatible personal computer, Apple Macintosh computer or UNIX-based workstation, as described above, the personal computer of Bowman-Amuah includes only a single processor ("CPU" of Bowman-Amuah, figure 1). Applicants do not find any mention in Bowman-Amuah of a vehicle having a gateway node including multiple processors. Thus, Bowman-Amuah fails to disclose a vehicle
20 having a gateway node that includes a first processor and a second processor, as claimed in amended claim 1. Bowman-Amuah further fails to disclose a first processor performing real-time processes and a second processor performing remaining processes other than the real-time processes.

Thus, for the reasons stated above, Applicants respectfully submit that claim 1, as
25 amended, is patentable over Bowman-Amuah. Additionally, as claims 2-10, 15-18, 21, 25-32, 34-47, and 52-65 depend from amended claim 1, claims 2-10, 15-18, 21, 25-32, 34-47, and 52-65 are patentable over Bowman-Amuah. Furthermore, as amended claims 66-68 include limitations similar to those of amended claim 1, claims 66-68 as amended are also patentable over Bowman-Amuah. Accordingly, Applicants respectfully request
30 withdrawal of the rejection under 35 USC §102(c).

Claim Rejections Under 35 USC §103

Claims 11-14, 19, and 20 are rejected under 35 USC §103(a) as being unpatentable over Bowman-Amuah in view of Bergkvist, Jr. et al., United States Patent number 5,535,380 ("Bergkvist"). Claims 22-24 are rejected under 35 USC §103(a) as being unpatentable over Bowman-Amuah in view of Kirby, United States Patent number 6,829,437 ("Kirby"). Claim 33 is rejected under 35 USC §103(a) as being unpatentable over Bowman-Amuah in view of Cox et al., United States Patent number 6,738,814 ("Cox"). Claims 48-50 are rejected under 35 USC §103(a) as being unpatentable over Bowman-Amuah in view of Vasudevan et al., United States Patent number 6,715,077 ("Vasudevan"). Claim 51 is rejected under 35 USC §103(a) as being unpatentable over Bowman-Amuah in view of Chittor et al., United States Patent number 5,987,552 ("Chittor").

Applicants respectfully submit that claim 1, as amended, is non-obvious and patentable over Bowman-Amuah for the reasons stated above. As claims 11-14, 19, 20, 22-24, 33, and 48-51 depend from amended claim 1, and amended claim 1 is non-obvious and patentable in view of Bowman-Amuah, claims 11-14, 19, 20, 22-24, 33, and 48-51 are non-obvious and patentable over Bowman-Amuah in view of Bergkvist, Kirby, Cox, Vasudevan, and/or Chittor in any combination. Applicants also submit that claims 11-14, 19, 20, 22-24, 33, and 48-51 would not have been obvious in view of Bowman-Amuah, Bergkvist, Kirby, Cox, Vasudevan, and/or Chittor in any combination for the reasons stated above with reference to amended claim 1. Accordingly, Applicants respectfully request withdrawal of the rejections under 35 USC §103(a).

Regarding the rejection of claim 11 in view of the combination of Bowman-Amuah and Bergkvist, Applicants respectfully submit that claim 11 defines "RTIP" as a real-time interface processor. In contrast to claim 11, Bergkvist uses "RTIP" to mean a real-time interrupt period (column 2, line 9). The real-time interrupt period disclosed by Bergkvist is for use with a single microprocessor in order to provide a programmable time-based interrupt which solves the latency problem through the use of a real time interrupt period register and one or more safe period registers. The RTIP registers of Bergkvist control the execution of types or classes of events which must be suppressed in order to minimize the latency of servicing an interrupt on time with a single processor.

and therefore make the single microprocessor appear to be doing real-time operations (column 2, lines 6-41).

Thus, the combination of Bowman-Amuah and Bergkvist actually teaches away from the invention of amended claim 11 because, as Bergkvist describes using registers 5 to eliminate unwanted latency in a single-processor system, the combination of Bowman-Amuah and Bergkvist does not disclose coupling a plurality of network elements in a vehicle including at least one node and at least one vehicle bus among at least one peripheral electronic device, wherein the at least one node includes at least one gateway node in the vehicle, the gateway node comprising a first processor performing real-time processes and a second processor performing remaining processes other than the real-time processes. The combination of Bowman-Amuah and Bergkvist also does not disclose performing real-time operations using the first processor, wherein the first processor includes at least one real-time interface processor (RTIP), and performing high level processing functions using the second processor, wherein the second processor includes at least one application processor. Applicants therefore submit that claim 11 is non-obvious and patentable over Bowman-Amuah in view of Bergkvist
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Conclusion

In view of the foregoing amendments and remarks, Applicants respectfully submit 20 that claims 1-68 as amended herein are in condition for allowance. Thus, allowance of the claims is requested. If in the opinion of Examiner Moorthy a telephone conference would expedite the prosecution of the subject application, or if there are any issues that remain to be resolved prior to allowance of the claims, Examiner Moorthy is encouraged to call Rick Gregory at (408) 342-1900.

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Extension of Time

A Petition for Extension of Time Under 37 CFR 1.136(a) is enclosed herewith in duplicate for a two (2) month extension of time.

5 Authorization to Charge Deposit Account

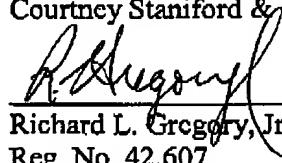
Please charge deposit account 503616 for any fees due in connection with this Office Action response.

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Respectfully submitted,
Courtney Staniford & Gregory LLP

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